REMARKS

Claims 1-3, 5, 8, and 9 remain in the application for consideration of the Examiner with Claims 4, 6, 7, and 11-16 standing cancelled.

Reconsideration and withdrawal of the outstanding rejections are respectfully requested in light of the above amendments and following remarks.

The drawings were objected to under 37 CFR 1.83(a).

The drawings were objected to because Figure 1 isn't clear.

The drawings were objected to because the interconnection between the figures is not shown.

These objections appear to be for the most part interrelated and are taken together.

The sensing unit corresponds to element 11, the integrator to element 23 and the generation unit to 13.

Furthermore, element 11 is shown as a differential amplifier arrangement, element 13 is a circuit to generate the target BMEF, element 22 is shown in Figure 2 as a capacitor, element 25 and element 26 are shown as switches, element 31 is shown as a capacitor, and element 32 is shown as a reset switch.

Furthermore, with respect to Figure 2 and the SC controllers there is a clear relationship with Figure 1.

Additionally, there is no requirement that arrows should touch the object it is pointing to.

The specification was objected to as failing to provide a proper antecedent basis for the claimed subject matter.

This objection is traversed.

For the reasons set forth herein and above, the sensing unit, generation unit, and cancellation circuit are in the specification.

Claim 11 was objected to.

By the instant amendment, Claim 11 has been cancelled.

Claims 2, 3, 6-9, and 12-16 were rejected under 35 U.S.C. § 112, first paragraph.

This rejection is traversed.

For the most part, this appears to be a continuation of the objection to the specification and drawings.

Turning now to the art rejections, Claims 1-16 were rejected under 35 U.S.C. § 102(b) as being anticipated by Brito; and Claims 1-16 were rejected under 35 U.S.C. § 102(b) as being anticipated by Nakatani.

These rejections are respectfully traversed.

It is respectfully submitted that Brito does not disclose or suggest the presently claimed invention including a cancellation circuit coupled with the sensing unit and operable for determining a DC offset and providing said DC offset to said integrator part for said mathematical integration for canceling said DC offset from said compensation signal.

Brito discloses capacitor 194 however, this does not provide DC cancellation to the integration part.

It is respectfully submitted that Nakatani does not disclose or suggest the presently claimed invention including the integration part including a cancellation circuit coupled with the sensing unit and operable for determining a DC offset and providing said DC offset to said integrator part for said mathematical integration for canceling said DC offset from said compensation signal.

The Examiner alleges that element 112 in Figure 13a discloses this element.

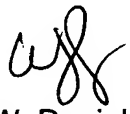
The offset value 110 is not provided to the integrator 112 and consequently could not disclose the above mentioned subject matter.

In light of the above, it is respectfully submitted that the present application is in condition for allowance, and notice to that effect is respectfully requested.

While it is believed that the instant response places the application in condition for allowance, should the Examiner have any further comments or suggestions, it is respectfully requested that the Examiner contact the undersigned in order to expeditiously resolve any outstanding issues.

To the extent necessary, Applicant petitions for an Extension of Time under 37 CFR 1.136. Please charge any fees in connection with the filing of this paper, including extension of time fees, to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.

Respectfully submitted,



W. Daniel Swayze, Jr.
Attorney for Applicant
Reg. No. 34,478

Texas Instruments Incorporated
P.O. Box 655474, MS 3999
Dallas, TX 75265
(972) 917-5633